



## Application of Fault Tree Analysis to Fuel cell diagnosis

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Keywords:	SOFC, PEFC, Diagnosis, Degradation, Fuel Cell

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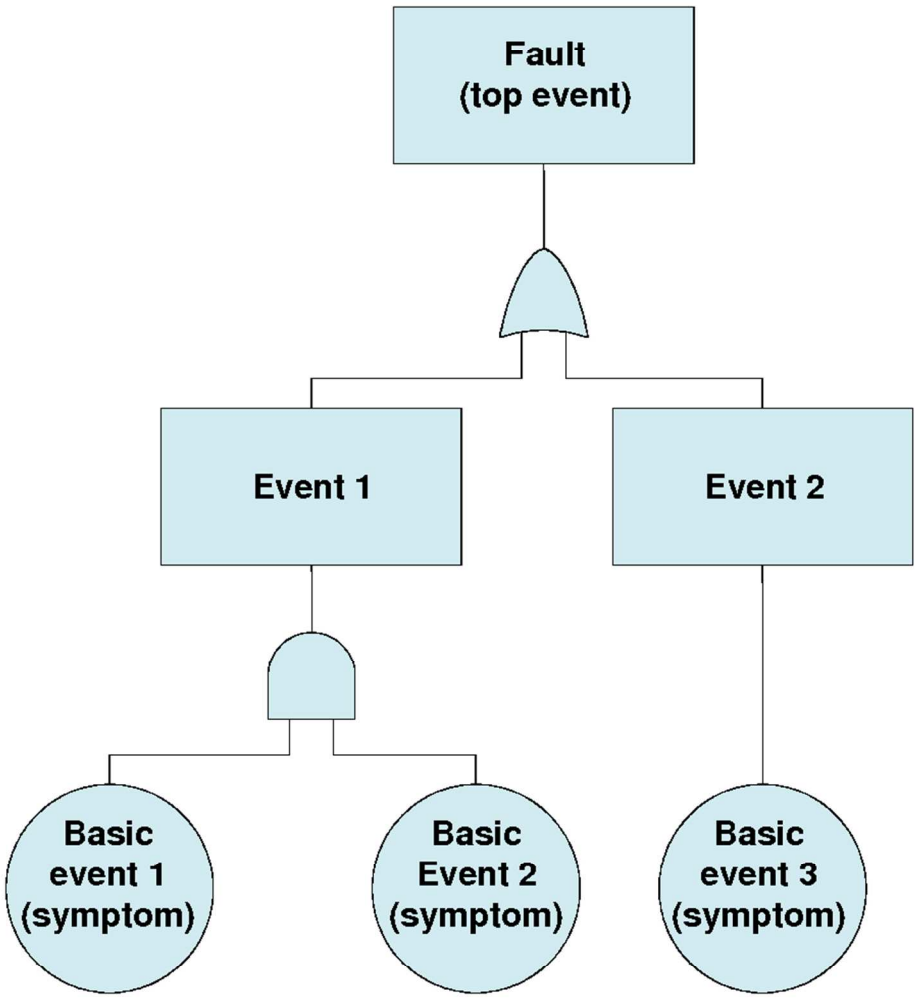


Fig. 1 Example of fault tree [6].  
83x92mm (300 x 300 DPI)


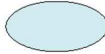





	<b>basic event / symptom</b>	<b>A basic initiating fault requiring no further development or the corresponding symptom</b>
	<b>conditioning event</b>	<b>Specific conditions or restrictions that apply to any logic gate</b>
	<b>undeveloped event</b>	<b>An event which is no further developed either because it is of insufficient consequence or because information is unavailable</b>
	<b>top/intermediate event</b>	<b>A fault event that occurs because of one or more antecedent causes acting through logic gates</b>
	<b>transfer</b>	<b>Indicates that the tree is developed at the occurrence in other pages. it is used to avoid extensive duplication in a fault tree</b>
	<b>and</b>	<b>Output fault occurs if all of the input faults occur</b>
	<b>or</b>	<b>Output fault occurs if at least one of the input faults occurs</b>

Fig. 1 Example of fault tree [6].  
108x103mm (300 x 300 DPI)



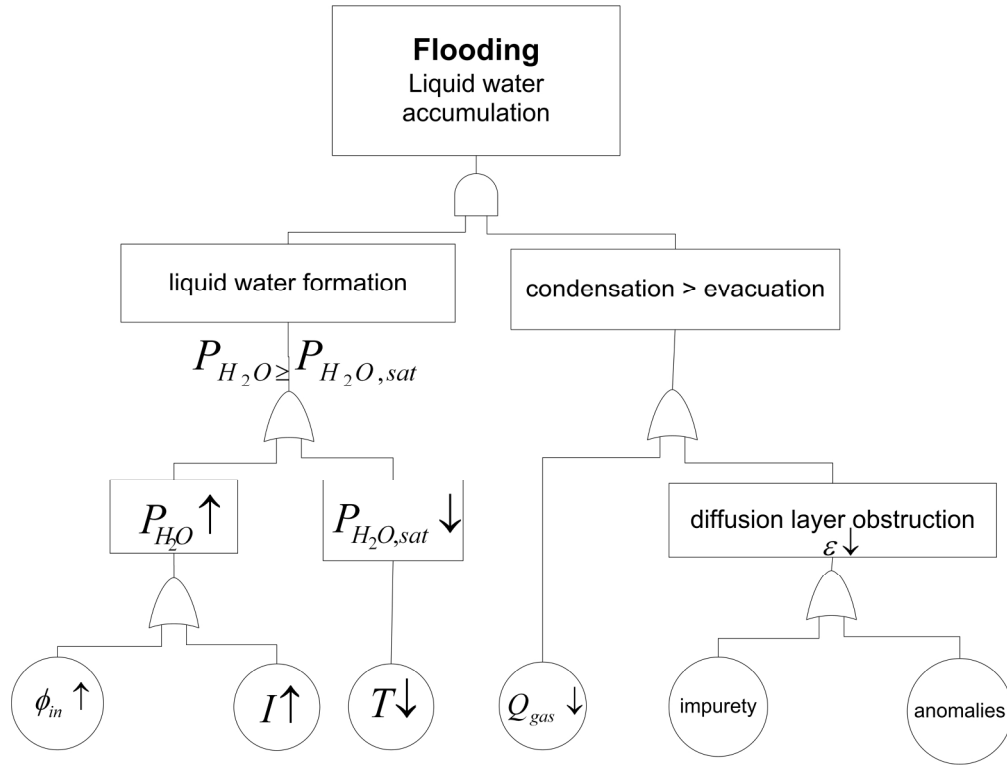


Fig. 2 Fault trees linked to (a) membrane drying out and (b) stack flooding [10].  
106x80mm (600 x 600 DPI)

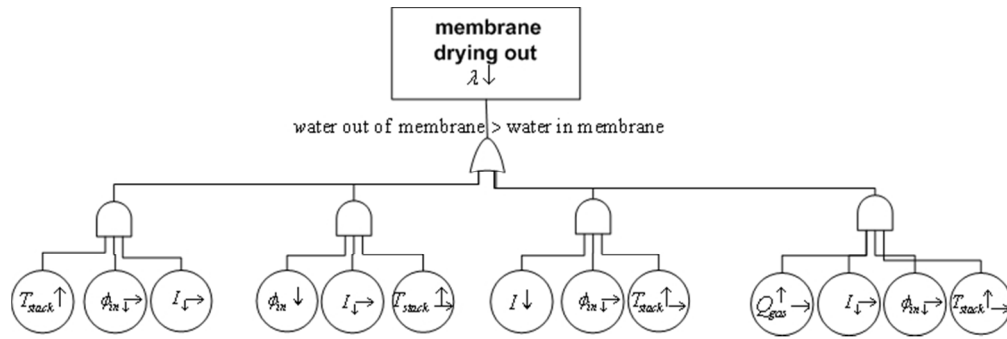


Fig. 2 Fault trees linked to (a) membrane drying out and (b) stack flooding [10].  
201x66mm (96 x 96 DPI)

Or Peer Review



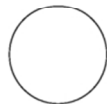
**Failure/Cause INTERMEDIATE EVENT** A fault event that occurs because one or more antecedent causes acting through logical gates



**« OR » Gate**, The causes that are can independently, bring about the undesired event are arrayed horizontally below the « OR » symbols



**« AND » Gate**, The causes that must exist simultaneously, to bring about the undesired event are arrayed horizontally below the « AND » symbols

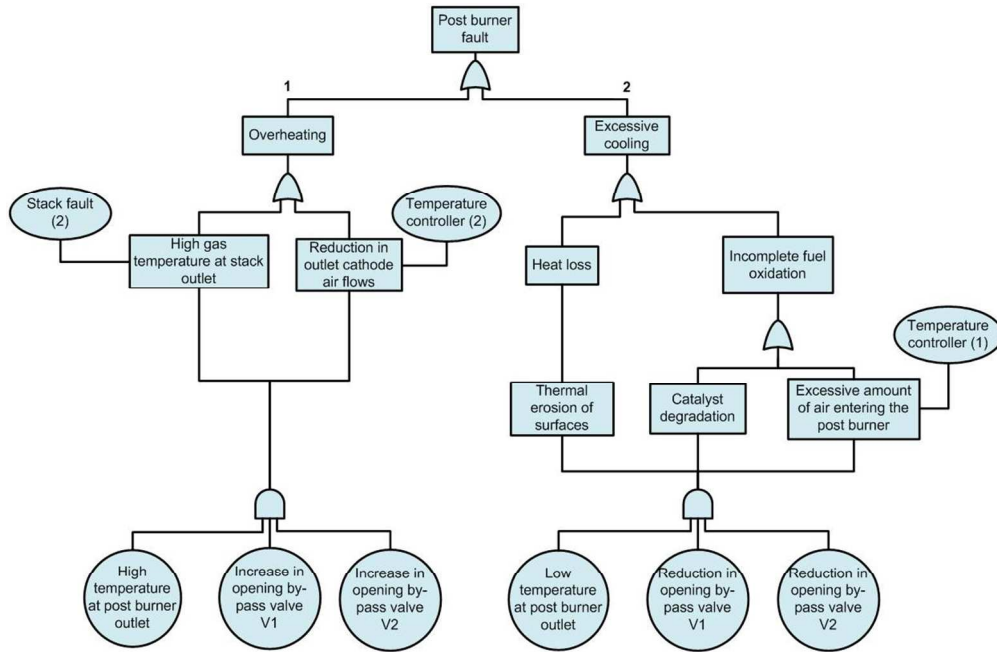


**« Basic Event »** Basic initiating fault requiring no further development

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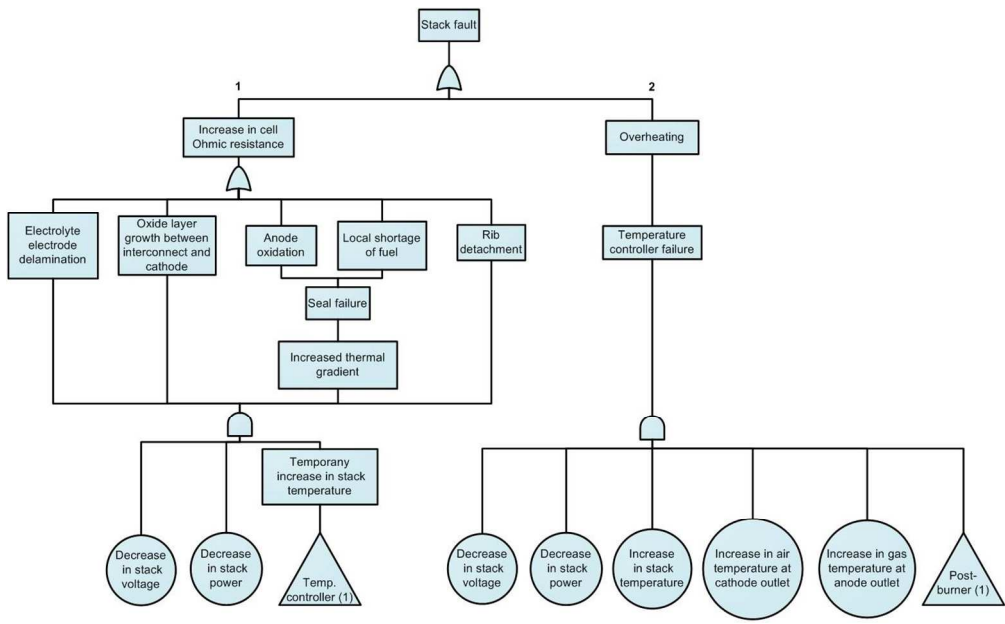
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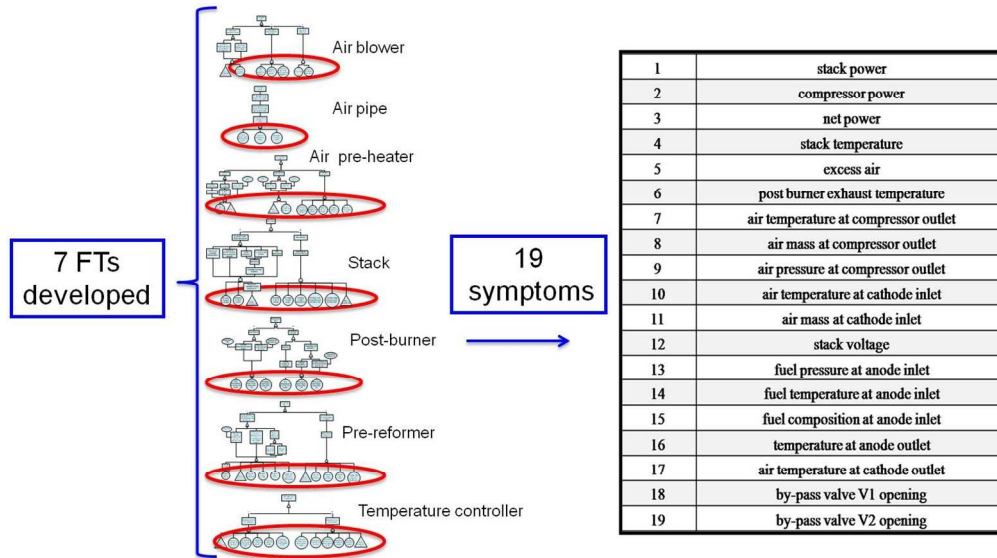


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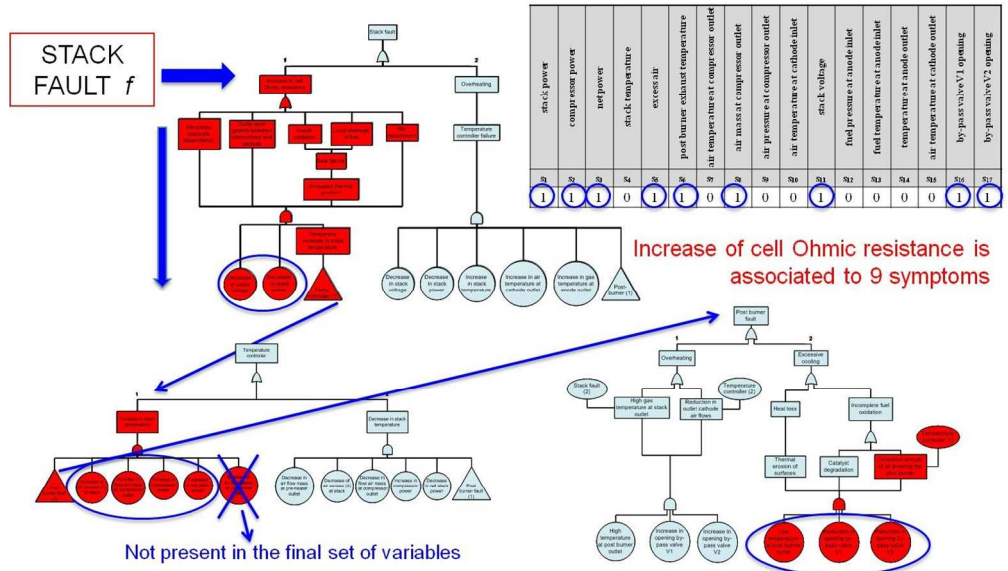


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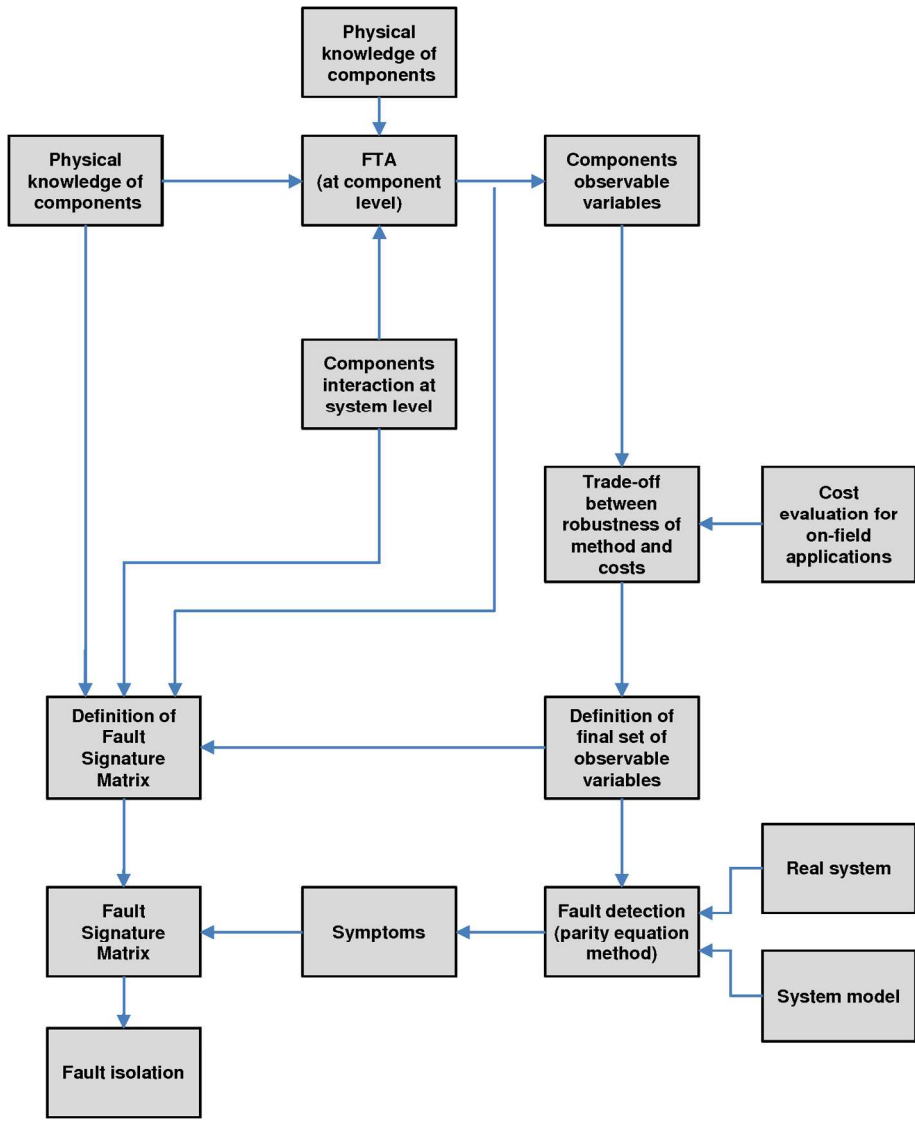
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		stack power		compressor power		net power		stack temperature		excess air		post burner exhaust temperature		air temperature at compressor outlet		air mass at compressor outlet		air pressure at compressor outlet		air temperature at cathode inlet		stack voltage		fuel pressure at anode inlet		fuel temperature at anode inlet		temperature at anode outlet		air temperature at cathode outlet		by-pass valve Y1 opening		by-pass valve Y2 opening	
		S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15	S16	S17																	
Air blower fault: increase in mechanical losses	<i>f1</i>	0	1	1	0	1	1	1	1	0	1	0	0	0	0	0	1	1																	
Air blower fault: air leakage in inlet compressor manifold	<i>f2</i>	0	1	1	0	0	0	0	0	1	0	0	0	0	0	0	0	0																	
Air pre-heater: air flow outlet cooling	<i>f3</i>	0	1	1	0	1	1	0	1	0	1	0	0	0	0	0	1	1																	
Air pre-heater: air leakage in inlet plenum	<i>f4</i>	0	1	1	0	0	0	0	1	0	1	0	0	0	0	0	0	0																	
Air pipe fault: air leakage	<i>f5</i>	0	1	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0																	
Pre-reformer fault: catalyst degradation	<i>f6</i>	1	1	1	0	1	1	0	1	0	0	1	1	1	0	0	1	1																	
Pre-reformer fault: heat exchange surface corrosion	<i>f7</i>	1	1	1	0	1	1	0	1	0	0	1	0	1	0	0	1	1																	
Post-burner fault: excessive cooling	<i>f8</i>	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	1																	
Stack fault: increase in cell Ohmic resistance	<i>f9</i>	1	1	1	0	1	1	0	1	0	0	1	0	0	0	0	1	1																	
Stack fault: overheating	<i>f10</i>	1	0	0	1	0	0	0	0	0	0	1	0	0	1	1	0	0																	

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# Application of Fault Tree Analysis to Fuel cell diagnosis

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## Abstract

Reliability and lifetime are common issues for the development and commercialization of fuel cells technologies'. As a consequence, their improvement is a major challenge and the last decade has experienced a growing interest in activities that aims at understanding the degradation mechanisms and at developing fuel cell systems diagnosis tools.

Fault Tree Analysis (FTA) is one of the deductive tools that allow "linking" an undesired state to a combination of lower-level events via a "top-down" approach which is mainly used in safety and reliability engineering.

The objective of this paper is to give an overview of the use and the contribution of Fault Tree Analysis (FTA) to both SOFC and PEFC diagnosis.

**Keywords:** Fuel Cells, SOFC, PEFC, Diagnosis, Degradation, Fault Tree Analysis.

## 1 Introduction

The needs of a better understanding of fuel cells degradation mechanisms and developing fuel cell diagnosis tools arose from the targets fixed by US department of Energy (DoE), European Union and Japan. Indeed, for transportation applications, the target is 5000h for the full range of external environmental conditions, which is the minimum requirement for vehicles in practical use. For stationary applications, the target is fixed at 40 000h of operation [1]. These objectives are defined for 2010 by the DoE and for 2015 by the European Union and by the NEDO (New Energy and Industrial Technology Development Organization) in Japan.

To achieve these objectives, an adequate diagnosis procedure has to be defined and set up. This requires a full understanding of degradation mechanisms and a proper definition of the parameters involved in the fuel cell operation and degradation. Indeed, a good knowledge of the influent parameters in a degradation phenomenon as well as their interactions is a mandatory step that allows properly defining adapted mitigation strategies.

Fault Tree Analysis (FTA) is one of the deductive tools that allow linking an undesired state to a combination of lower-level events via a "top-down" approach.

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3 The application of FTA for fuels cells diagnosis has also been performed in the frame of two projects:  
4 DIAPASON [2] (non intrusive diagnosis of PEFC systems) and GENIUS [3] (GENeric diagNosis InstrUMENT for  
5 SOFC Systems), respectively funded by the French ANR (Agence Nationale de la Recherche) and the European  
6 Commission through its Join Technology Initiative (JTI) program Fuel Cell and Hydrogen Joint Undertaking.

7  
8 Other works developed fault trees where the occurrence of events could be taken into account and the failure rate  
9 can be estimated using an experiments feedback [10] [9].

10  
11 The first part of this work presents some generalities about the Fault Tree Analysis. Then, the application of FTA  
12 to PEFC and SOFC is proposed in paragraphs 3 and 4. Finally, paragraph 5 concludes this paper.

## 13 14 15 16 2 Generalities about Fault Tree Analysis

17 In operating safety and reliability science, the Fault Tree Analysis is widely used tool to clear out the  
18 contributions of different parameters in an undesired event [4][5]. A fault tree is defined as a graphical  
19 representation of the relationship between an undesired event (called a top event) and all its potential causes. The  
20 analysis proceeds in a “top-down” approach, starting with the top event (failure, malfunction...) and determining  
21 all the causes that can lead to it. It determines how these top events can be caused by individual or combined  
22 lower level failures or events.

23  
24 An example of a fault tree is given by Figure 1 (a) [6]. In this figure, the fault (top event) can be caused either by  
25 event 1 or event 2. The event 1 is caused by the contemporary occurrence of the basic events 1 and 2 (i.e.  
26 symptoms). On the other hand, the event 2 is correlated to just one symptom (i.e. basic event 3).

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31 (Figure 1a) (Figure 1b)

## 32 33 34 35 3 Application to PEFC stacks

36 In a review of degradation linked to water management in PEFCs, N. Yousfi Steiner et al. [7] developed fault  
37 trees for the issues linked to water management (stack flooding and membrane drying out) (Figure 2a) and  
38 flooding (Figure 2b) in PEFC.

39  
40 In fact, stack flooding and membrane drying out are among the most frequently encountered faults in PEFCs  
41 stacks. Several works dealing with water management issues have been published these last few years [8].

42 These issues are mainly related to disequilibrium in water balance inside the fuel cell. A high temperature and  
43 high gas flow contribute to mitigate flooding by promoting liquid water evaporation favoring membrane drying  
44 out, while high current density results in high water production that promotes flooding just like low flow rate and  
45 high relative humidity.

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50 (Figure 2a) (Figure 2b) (Figure 2c)

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53 The main objective of these fault trees is to clear out the relationships existing between operating parameters and  
54 those given fault. The complexity of the PEFCs systems and the existence of a strong interaction among different  
55 variables make building fault trees very useful to clear out these relationships and to select relevant parameters  
56 for control and diagnosis.

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3 The isolated operating parameters in the “final leaves” of the trees are used to feed neural models aiming at  
4 diagnosing these issues. More details about the models could be found in [9].  
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## 7 4 Application to diagnosis of SOFC stacks and systems 8

### 9 4.1 Stack level 10

11 In SOFC stacks, the increase in cell ohmic resistance can be caused by electrode delamination, local shortage of  
12 fuel, anode oxidation or oxide layer growth and rib detachment.  
13

14 Electrode delamination consists of the detachment of one of the electrodes from the electrolyte, which is due to  
15 the mismatch in thermal expansion characteristics of the cell's different layers during thermal cycling.  
16 Delamination has severe consequences on the performance, since it increases the ohmic resistance of the cell  
17 proportionally to the delaminated area. Furthermore, it makes the affected area electrochemically inactive as a  
18 consequence of the high aspect ratio of the cell, which inhibits the transport of ions in the in-plane directions  
19 [10].  
20

21 Local shortage of fuel and anode oxidation can be caused by seal failures, which can occur at the interfaces  
22 during heating at start-up and cooling at shutdown, due to the mismatch in thermal expansion between metal and  
23 ceramic SOFC component. A sealing failure in SOFC stack causes gas leakage, which in turn, decreases the  
24 overall performance and lifetime of the stack.  
25

26 Oxide layer growth between the interconnect and the cathode is the growth of an electrically less conductive  
27 oxide layer between the interconnect plate and the electrodes, especially the cathode [10]. Chromium layers  
28 grow on the interconnect surface, degrading the electrical conductivity of the interconnect-electrode interface.  
29 This oxide layer is dense and adherent, providing the substrate protection against further oxidation.  
30

31 Rib detachment is the deformation of the planar cell components, with detrimental consequences associated with  
32 contact degradation between the interconnect and the electrodes, due to the thermal excursions. This  
33 phenomenon is referred to as rib detachment, and together with oxide layer growth causes an increase in the total  
34 resistance of the cell [11].  
35

36 On the other hand, the excessive overheating, which could be caused by a malfunction in the temperature  
37 controller or a lack in the air flow mass, due to an air blower fault is indicated by the increase in the temperature  
38 stack, which could lead to system failure.  
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40 The above mentioned faults related to the SOFC stacks and their interactions are summarized in the fault tree of  
41 Figure 3.  
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48 (Figure 3)  
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### 50 4.2 System level 51

52 Arsie et al. [6] applied fault tree analysis to SOFC, taking into account many components at system level (e.g. air  
53 blower, air pre-heater, pre-reformer, post-burner, air leakage in pipe connecting air blower and air pre heater and  
54 to stack).  
55

56 The FTA is proposed in this case as a tool for fault isolation process and the knowledge gained through it is  
57 exploited to understand the mutual interactions among all the devices within the entire SOFC system. Each of  
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3 the above mentioned faults is correlated, via a top-down approach, to corresponding symptoms (in total 7 fault  
4 trees were built) and a fault signature that links conveniently system-level symptoms to specific component  
5 faults has been defined.  
6

7 Figure 4 presents, for instance, the fault tree associated with the post-burner.  
8

9  
10 (Figure 4)

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12 The post-burner is used to complete the oxidation of the residual fuel at anode outlet and, in some systems, also  
13 to preheat the FC system during start-up. The main faults considered for this component are the overheating  
14 (branch 1) and cooling (branch 2). Following a top-down approach, 6 symptoms of this undesired event are  
15 isolated. They represent the “leaves of the fault tree presented in Figure 4.”  
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19 After developing a fault tree for each system component, a set of system variables is identified as shown in  
20 Figure 5.  
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24 (Figure 5)

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26 A final set of 17 variables is selected according to the trade-off between the robustness of the method and the  
27 cost evaluation for the in-field implementation (Table 2).  
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31 (Table 2)

### 32 Building a fault matrix

33 According to the FTA at component level and the knowledge of components interactions at system level, each  
34 fault is associated with a vector of symptoms, which contains 1 if the symptom is associated with that fault and 0  
35 otherwise.  
36

37 The procedure is illustrated in Figure 6: starting from the fault, through an approach top/down, all the symptoms  
38 (circles) linked to the fault are individuated. The symptoms considered are both those of the specific component  
39 fault tree, and all the other ones present in the related components fault tree (through the triangle).  
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44 (Figure 6)

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46 The procedure must be repeated for each fault and every symptoms vector must be different from each other;  
47 thus it is possible to isolate univocally a fault. Once all the faults are considered it is possible to generate a so-  
48 called Fault Signature Matrix (FSM). The FSM has in rows the system faults and in columns, the symptoms  
49 associated to define a set of variables (Figure 7).  
50

51  
52  
53 (Figure 7)

54  
55 According to this approach, residual are generated by comparing the defined set of system variables observed in  
56 the real system and their equivalent ones in a system model, which simulates the system in nominal (non faulty)  
57 condition. The Fault Detection and Isolation (FDI) proposed in this work (Figure 8) is then based on the analysis  
58 of these residuals. More details can be found in [6].  
59  
60



(Figure 8)

## 5 Conclusion

This paper has reviewed different works dealing with the application of Fault Tree Analysis to fuel cells stacks and systems.

The fault tree representation appeared to be a good way to clear out the connections and interactions between the different operating parameters responsible of a given fault. It allows a better understanding of degradation phenomena and a judicious choice of the impacting parameters. These parameters are then very useful to feed models in an optimized way or to construct Fault Signature Matrixes.

## Acknowledgement

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## References

- [1] DOE "Department of Energy", US Department Of Energy, **2004**.
- [2] DIAPASON, Agence Nationale de Recherche ed., Plan d'action National sur l'Hydrogène et les piles à combustible (PAN-H), **2006**.
- [3] GENIUS, <https://genius.eifer.uni-karlsruhe.de>, **2010-2013**.
- [4] R. Isermann, *Fault Diagnosis System, An Introduction from Fault Detection to Fault Tolerant*, Springer, Verlag, Berlin Heidelberg, Germany, **2006**.
- [5] Y. Mortureux, in *Techniques de l'Ingénieur: Sécurité et gestion du risque, Vol. SE4010*, **2002**, pp. 1-24.
- [6] I. Arsie, A. Di Filippi, D. Marra, C. Pianese, M. Sorrentino, in *ASME 2010. Eight International Fuel Cell Conference, Engineering and Technology, Brooklyn, New York*, **2010**.
- [7] N. Yousfi Steiner, P. Moçotéguy, D. Candusso, D. Hissel, A. Hernandez, A. Aslanides, *J. Power Sources* **2008**, *183*, 260-274.
- [8] aF. A. de Bruijn, V. A. T. Dam, G. J. M. Janssen, *Fuel Cells* **2008**, *8*, 3-22; bJ. St-Pierre, *J. Electrochem. Soc.* **2007**, *154*, B724-B731; cJ. Stumper, M. Lohr, S. Hamada, *J. Power Sources* **2005**, *143*, 150-157.
- [9] N. Yousfi Steiner, D. Hissel, P. Moçotéguy, D. Candusso, *Int. J. Hydrogen Energy* **2011**, *36*, 3067-3075.
- [10] H. Yokokawa, H. Tu, B. Iwanschitz, A. Mai, *J. Power Sources* **2008**, *182*, 400-412.
- [11] J. I. Gazzarri, O. Kesler, *J. Power Sources* **2008**, *176*, 138-154.

## Figures captions

Fig. 1 (a) Example of fault tree (b) legend.

Fig. 2 Fault trees linked to (a) membrane drying out and (b) stack flooding (c) symbols.

Fig. 3 Fault tree for the SOFC stack.

Fig. 4 Fault tree for the post burner.

Fig. 5 Procedure to select symptoms from fault trees.

Fig. 6 Procedure to generate fault/symptoms association vector.

Fig. 7 Example of Fault Signature Matrix (FSM).

Fig. 8 FDI process scheme.

## Tables

Table 1 Nomenclature.

Variable	Description
$P_x$	Partial pressure of x [Pa]
$\Phi_{in}$	Relative humidity of the inlet gases [%]
$I$	Current [A]
$T, T_{stack}$	Stack Temperature [°C]
$Q_{gas}$	Volumetric flow rate [m <sup>3</sup> .s <sup>-1</sup> ]
$\varepsilon$	Electrode porosity
$\lambda$	Membrane water content

Table 2 Final set of variables to be monitored in the system.

1	stack power [kW]
2	compressor power [kW]
3	net power [kW]
4	stack temperature [°C]
5	excess air [l]
6	post burner exhaust temperature [°C]
7	air temperature at compressor outlet [°C]
8	air mass at compressor outlet [kg/s]
9	air pressure at compressor outlet [Pa]
10	air temperature at cathode inlet [°C]
11	stack voltage [V]
12	fuel pressure at anode inlet [Pa]
13	fuel temperature at anode inlet [°C]
14	temperature at anode outlet [°C]
15	air temperature at cathode outlet [°C]
16	by-pass valve V1 opening [%]
17	by-pass valve V2 opening [%]