

Fast contactless vibrating structure characterization using real time FPGA-based digital signal processing: demonstrations with a passive wireless acoustic delay line probe and vision

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Vibrating mechanical structure characterization is demonstrated using contact-less techniques best suited for mobile and rotating equipments. Fast measurement rates are achieved using Field Programmable Gate Array (FPGA) devices as real-time digital signal processors. Two kinds of algorithms are implemented on FPGA and experimentally validated in the case of the vibrating tuning fork. A first application concerns in-plane displacement detection by vision with sampling rates above 10 kHz, thus reaching frequency ranges above the audio range. A second demonstration concerns pulsed-RADAR cooperative target phase detection and is applied to radiofrequency acoustic transducers used as passive wireless strain gauges. In this case, the 250 ksamples/s refresh rate achieved is only limited by the acoustic sensor design but not by the detection bandwidth. These realizations illustrate the efficiency, interest and potentialities of FPGA-based real-time digital signal processing for the contact-less interrogation of passive embedded probes with high refresh rates.

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I. INTRODUCTION

Vibrating structure characterization is classically performed to assess mechanical equipment characteristics or as part of preventive maintenance. On rotating or moving parts, a wired solution is not robust for long term monitoring, with sliding contact or rotating optical couplers¹ being demonstrated in the literature for laboratory experiments. One alternative to structure bending and vibration analysis – whether applied to macroscopic^{2,3} or microscopic structures^{4,5} – is image processing⁶ in which the structure deformation and motion is detected either through motion of its natural surface properties (roughness yielding varying reflected light intensity) or artificially patterning the surface with a structure best suited to motion detection⁷. Embedding strain gauges on the mobile part with wireless communication yet on-board energy source⁸ reduces the life expectancy of the measurement setup to the battery life of the powered sensor and hence requires periodic maintenance⁹: the broad field of wireless sensor networks (WSN) is concerned with such an approach¹⁰⁻¹³. Only very few studies are concerned with embedding passive sensors in the vibrating structure and using variations of the reflected signal when illuminated by RADAR signals¹⁴ – either through a RADAR Cross-Section (RCS) variation or resonance frequency variation¹⁵⁻¹⁷ – to measure strain in mechanical structures.

We here consider two contact-less vibrating structure characterization means: digital image processing and using piezoelectric acoustic delay lines acting as passive strain gauges probed through a wireless link. In the context of control, real time analysis of the recorded data is mandatory and postprocessing of large records is not an option. Furthermore, in the context of vibrational analysis, measurement speed is a significant indicator of sensor performance in order to extend to the highest vibration modes the analysis of the mechanical structure under investigation. As such, a real time fast measurement technique, as opposed to as stroboscopic approach yielding high equivalent sampling rate but assuming a periodic signal, allows for the detection of transient events with short durations¹⁸. While the envisioned algorithms are basic and have been described previously, the implementation on hardware compatible with real time feedback control is challenging, and is the topic of this paper.

Real time signal processing has long been devoted to analog processing, with the utmost bandwidth being achieved by piezoelectric and optical physical signal processing including

convolution, correlation and Fourier transform¹⁹. The advent of digital signal processing yielded long term stability (no dependence of the analog component value to environmental conditions) and reconfigurability²⁰ has become the mainstream implementation strategy, with the loss of real time signal processing when software and operating systems are involved. Field programmable gate arrays (FPGA) provide the means of achieving the best of both worlds, with digital signal processing running at radiofrequency rates. One current application of such an approach is concerned with Software Defined Radio aiming at sampling the baseband radiofrequency signal and performing all signal processing as software defined algorithms. In these demonstrations, we implement one example of digital image processing for motion detection with frame rates reaching the ksample rate (i.e. many thousand images recorded and processed every second) and a pulsed mode RADAR operating in the 2.45 GHz aimed at probing a surface acoustic transducer acting as a passive, wireless strain sensor. The latter measurement technique is the result of a lengthy evolution²¹ in which a piezoelectric substrate is used to convert an incoming electromagnetic pulse to a mechanical (acoustic) wave: the environmental (temperature, strain) effect on the acoustic wave velocity modifies the time of flight of the acoustic pulse which, once converted back to an electromagnetic wave, is recorded by the RADAR receiver²². A measurement of the time of flight allows for the recovery of the acoustic velocity and hence the physical property most significantly affecting this velocity as selected by proper sensor design (minimizing unwanted physical property effect while emphasizing the dependence with the considered quantity). While most RADAR approaches have been developed with power consumption or computational limitations considerations, with the Frequency Modulated Continuous Wave approach (FMCW)²³ being the most basic implementation, high bandwidth measurements aimed at matching at best the performance of the sensor have not been presented in the literature. Our pulsed RADAR setup aims at using at best digital signal processing implemented on FPGA for performing a wireless measurement technique of probing acoustic delay lines, only limited in measurement bandwidth by the acoustic propagation time (physical measurement principle), and not by the means of measuring the signal. Pulse mode interrogation of wireless acoustic sensors has been demonstrated previously in academic settings^{24,25} but is still not used in commercial embodiments of such interrogation units applied to acoustic delay lines²⁶. While passive target motion has been detected wirelessly with high bandwidth by using Doppler RADAR^{27,28}, actual remote strain measurement has not been achieved

by such means with the use of a dedicated transducer attached to the probed structure. Torque measurement using acoustic transducers acting as passive strain gauges attached to the structure under investigation has been investigated in-depth by Transense^{29,30} and in³¹.

II. VISUAL DETECTION OF IN-PLANE DISPLACEMENTS

A. Measurement principle

Optical measurement of vibrating structure is the most intuitive approach to contactless displacement detection. Various out of plane displacement measurement methods range from speckle interferometry detection to holographic displacement detection, while in-plane displacement is tackled by imaging³² and photogrammetry. The latter approach will be of interest to us here, with the specific case of a quantitative displacement analysis by using a periodic pattern attached to the vibrating structure^{33,34}: the known periodicity yields a quantitative in-plane displacement measurement.

In-plane detection is usually performed as off-line processing due to the computationally intensive processing requirements of correlation methods⁷. Furthermore, reaching sub-pixel resolution additionally requires some curve fitting or interpolation. We here first describe the basics of applying a cross-correlation method for detecting the motion of a patterned target (Fig. 1) fixed to the moving prong of a vibrating tuning fork³³. We then demonstrate the implementation of this algorithm in a FPGA for real time computation of the target position.

For a given magnification factor, the analysis function is defined by the length L of the sample (image length in pixels) and the period p of the pattern (in pixels). We furthermore window the analysis function by a Gaussian envelope in order to generate the analysis function:

$$\text{analysis} = \exp\left(-\left(\frac{x}{4p\sqrt{2}}\right)^2\right) \times \exp\left(i \times x \frac{2\pi}{p} + \varphi_0\right)$$

with a pixel range $x = [1..L] - \bar{x}$ with L the line length being analyzed and \bar{x} the midpoint of this line. In our case, $L = 1024$, $p = 32$ and $\bar{x} = 512$. φ_0 is a constant selected to avoid 2π rotation when computing the phase φ of the cross-correlation which is performed in the spatial domain as $\varphi = \text{arg}(\sum_{k=[1..L]} \text{measurement}_k \times \text{analysis}_k)$. This phase is representative of the spatial position of the pattern, with a 2π phase jumps associated with a one-period

displacement of the reference grid. Hence, this analysis is quantitative with p only given by the optical setup magnification factor and the actual grid periodicity. Typical resolutions achieved by phase analysis and interpolating the unwrapped phase are in the range of 10^{-2} pixel or 10^{-3} grid period³³, *i.e.* providing 10 nm positioning resolution when using visible illumination and low magnification lenses ($\times 10$ or $\times 20$). This sub-pixel resolution is achieved by the integral (sum) over multiple periods of the convolution of the image with the analysis function which has been tuned to match the period for a known pattern observed at a known magnification: the longer the observed line length (parameter L), the better the achievable resolution, for a given camera resolution and a given illumination condition (*i.e.* given signal to noise ratio of the recorded pixels).

B. Real time processing: FPGA implementation and experimental results

A Spartan 6 FPGA shares common data, address and control busses with an i.MX27 CPU as provided on a custom design based on the Armadeus Systems (Mulhouse, France) APF27 board. The FPGA is in charge of real time processing of the data as the pixels flow in through the serializer-deserializer interface, while the CPU is only used for post-processing the resulting data, namely computing the ratio of the imaginary to the real part of the complex output of the algorithm, computing the arctan of the result to reach an angle, and transferring the data to the user through an ethernet link.

[FIG. 1 about here.]

The imaging source is a PhotonFocus MVD1024E160 camera communicating through a CameraLink interface. Thus, the data source is the LVDS interface in charge of feeding the processing chain with 8-bit wide pixel values transferred as pairs at every clock cycle. Considering that the CameraLink digital interface clock speed is 80 MHz and 2 bytes are provided at every clock cycle, the use of a 12-pixel wide (for averaging and thus noise reduction) and 1024-pixel long region of interest out of the available 1024×1024 pixel size yields a data transmission duration of $76.8 \mu\text{s}$ per frame. This camera hardware transfer time, in addition to the exposure duration, limits the frame rate. In addition, each line ends with a 100 ns delay, while before each frame the exposure time is set to the minimum value thanks to a bright LED light source, namely $10 \mu\text{s}$. The practical frame rate meets the

theoretical threshold of 5285 frame/s (Fig. 2), only limited by the camera data transfer rate since all processing operations are performed along the pipeline described in Fig. 1. This implementation is able to receive two consecutive pixels at each clock cycle and produce one phase output value one cycle after the end of image transmission. However, this analysis emphasizes a significant limitation due to the camera orientation since the pattern is varying along the columns. Each end of line induces a delay of 100 ns accumulating, for a 1024-line long image, up to 102.4 μ s. Since each line transfer completion requires a 100 ns delay, two implementations of the digital signal processing blocks are compared, one in which the pattern is along the acquisition line and one in which the pattern is along the acquisition column. Since the camera provides at each clock cycle adjacent pixels, the former approach illustrated in Fig. 1 (b) requires a common analysis coefficient while the latter approach in Fig. 1 (c) requires two different analysis coefficients since adjacent pixels are not located on the same line. This difference yields some inversion in the multiplier and accumulator pattern as seen by comparing Figs. 1 (b) and (c).

By rotating the camera by an angle of 90° , the latencies due to the end of lines are reduced to the number of pixels parallel to each periodic pattern line. In our case, by averaging over 12 pixel wide lines, the 102.4 μ s delay is reduced to 1.2 μ s. Both approaches yield similar implementations and since phase computation derived from pixel averages and phase averaging computed on each column are linear operations, the two approaches yield results with the same resolution. However, with the latter approach, the frame rate increases up to 11360 fps. Removing the averaging along the 12-pixel wide line still increases the update rate up to 60610 fps, at the cost of reduced signal to noise ratio. In the latter case, the exposure duration of 10 μ s accounts for more than half of the frame acquisition duration and becomes the limiting factor defining the acquisition rate. The camera orientation allowing for maximum bandwidth is used to generate Fig. 3.

The implementation of the signal processing block aims at using at best the parallel processing capability of a FPGA (Fig. 1, right): since two adjacent pixels are transferred by the camera at each clock cycle, the averaging is first performed by the adder. The multiplication by the analysis function is performed on the result, which is then accumulated to compute the cross-correlation function along a line. The computation is performed in parallel on the real part (I-component) and the imaginary part (Q-component), with only the analysis function changing from $\cos(2\pi x/p)$ to $\sin(2\pi x/p)$.

Since the hardware multiplier and accumulator are optimized to operate on 18 bit values, the analysis function is scaled by 32765 to fit in a 16 bit signed integer value.

In order to avoid any limitation associated with digital data transfer to a general purpose CPU, the sink of the data is a digital to analog (D/A) converter output providing voltages proportional to the I and Q components of the cross correlation. Fig. 2 exhibit oscilloscope outputs of these computations as a music tuning fork is vibrated at 440 Hz through a magnetic excitation by a voice coil³². The digital oscilloscope traces are here recorded and the phase extraction is performed as post-processing, avoiding the need to implement on-line the $\arctan()$ lookup table function. In case of the use of such information for feedback control, the 2π phase jump induced by the displacement of the target by more than one period must additionally be handled to avoid the discontinuity as observed on Figs. 2 and 4, right.

[FIG. 2 about here.]

Having achieved periodic oscillation condition observation of the tuning fork prong motion at a frequency known to be close to one of the eigenmodes of the device, we consider the impulse response of the tuning fork as observed by means of digital image processing (Fig. 3). In this case, two modes are well identified, the fundamental mode around 440 Hz and one overtone close to 10 kHz.

[FIG. 3 about here.]

We have previously described the implementation of a fast measurement algorithm identifying the limitation on the measurement bandwidth when using an acoustic resonator transducer acting as a strain gauge³⁵: a practical implementation reaching this refresh rate has been demonstrated by another group in³⁶. Comparison of the resonator based measurement and the optical measurement, both of which are performed at 7 ksamples/s, is provided in Fig. 4. The quantitative analysis of the resonator based strain gauge measurement to generate an estimate of the prong displacement has been provided previously³⁵, while the optical periodic pattern image processing analysis directly provides a quantitative estimate of the prong end displacement. Fig. 4 exhibits on the one hand good agreement between the two measurement methods performed simultaneously, and emphasizes the limitation of

the optical measurement when large displacements are considered, yielding 2π phase rotations in the analysis (Fig. 4, right). For large displacements, the quartz strain gauge seems to overestimate the displacement, as has also been observed independently in quasi-static conditions, possibly due to the assumption on the tuning fork prong stress transfer to the piezoelectric substrate not being valid at high stress values. Indeed, in this test, the gold-coated backside of the acoustic resonator substrate was soldered using SnPb to the tuning fork, possibly inducing creep at high strain rates.

[FIG. 4 about here.]

III. REAL TIME ACOUSTIC DELAY LINE PROBING

A. Pulsed-mode RADAR interrogation of an acoustic delay line

Acoustic delay lines²² are one possible implementation of the broader range of acoustic sensors manufactured by patterning interdigitated electrodes on a single-crystal piezoelectric substrate. Various sensor designs – broadband delay lines, chirped delay lines, resonators – exhibit various advantages and drawbacks, providing the freedom of selecting design rules depending on the targeted property. When considering vibrating structure monitoring, refresh rate appears as the most significant parameter, with interrogation range often a secondary issue. While a resonator configuration exhibits low loss and hence extended interrogation range in a wireless sensing application in which the resonance frequency is representative of the physical quantity under investigation and monitored through a radiofrequency link, the time constant for loading and unloading energy limits the update rate to a few kilohertz as was discussed earlier³⁵ when operating in the 434 MHz range, while the faster expected refresh rate achievable when using resonators in the 2450 MHz range has not been demonstrated yet. On the other hand, an acoustic delay line converts an incoming electromagnetic pulse to an acoustic wave propagating on the single-crystal piezoelectric substrate at a velocity dependent on the physical property under investigation, and the two-way trip is recorded as a phase information of the reflection coefficient of the transducer probed through a wireless link³⁷. The lack of energy confinement and the poor efficiency of electromechanical conversion define the acoustic delay line losses which are typically in the 35 to 40 dB range, hence reducing the interrogation range with respect to the resonator configuration (Fig 5).

However, typical delay line tags exhibit a maximum echo delay of $4 \mu\text{s}$, or a 1.6 cm two-way acoustic trip assuming a 4000 m/s acoustic velocity on lithium niobate, and should thus be compatible with a 250 kHz refresh rate. Indeed, in the most basic embodiment of a RADAR interrogation unit, a short (as will be defined later) radiofrequency pulse is generated, echoes are recorded up to the maximum duration of the acoustic delay, and this process is repeated. The repetition rate is thus only limited by the acoustic delay line length which defines the maximum delay. In this example, the 8-bit tag is assumed to reflect all incoming energy within the $4 \mu\text{s}$ after excitation by the incoming pulse, and we will demonstrate the practical implementation of a sampling strategy providing the maximum possible refresh rate.

[FIG. 5 about here.]

The emitted pulse length is defined in the time domain by the separation of the echoes, which itself is typically set in order to comply with radiofrequency emission regulation in the frequency domain. Since the 2.45 GHz ISM band is 80 MHz wide, typical pulse widths of 25 ns will occupy a 40 MHz spectral width or half of the allocated band. In our experiments, 40 ns long pulses are generated as a tradeoff between radiofrequency switch toggling time constant (in our case, Minicircuits ZASWA-2-50DR+), maximizing transmitted energy, and separation of echoes. Ideally, the number of periods of the oscillating radiofrequency wave within the pulse should be equal to the number of interdigitated electrode pairs in the transducer. Although the FMCW approach has been classically selected for the low sampling rate of the receiver since audio-frequency beat signals between the emitted wave and the time-delayed echoes are generated, reaching the utmost bandwidth achievable by the transducer acoustic delay requires increasing the sweep rate, yielding increased beat frequency and hence increased sampling rate of the receiver analog to digital converter. Hence, whatever the considered architecture, high bandwidth signal processing necessarily requires high acquisition bandwidth, and we hence consider the pulsed mode RADAR approach as best suited to reach the maximum measurement speed since no additional processing (Fourier transform) is needed beyond recording the echoes received from the sensor.

B. FPGA implementation and results

The major implementation challenge of a fast reader is in recording the returned echoes, extracting the relevant information representative of the acoustic velocity, and repeating

the process fast enough to provide the user with a fast refresh rate. Recent availability of fast analog to digital converters and arrays of reconfigurable logic gates as provided in FPGAs provides the basic tools for implementing the considered measurement technique (Fig. 6). The FPGA controls all interrogation sequences: an internal counter triggers the switch for generating a 40 ns pulse, the dataflow from the 1 GS/s analog to digital converter (ADC, Hittite HMCAD1511) is fed to a serializer-deserializer stage as available in the Xilinx Spartan6 FPGA, and either the full time domain record of 2048 samples is transferred to the iMX27 CPU for processing and display in order to select the echo delay, or the sample of interest is stored in RAM for further processing. This acquisition and processing strategy is reminiscent of the previous camera-based data acquisition approach. Notice that 2048 samples recorded at 500 Msamples/s provide a record lasting 5 μ s or the total time delay of the acoustic transducer, while such a dataset fits within the RAMs internal to the FPGA. In the example we are demonstrating here, 2048 successive samples of the interrogation sequence are stored in RAM for display to the user as a delay line acting as a strain gauge glued on a 440 Hz musical tuning fork is probed. At 200 kHz refresh rate, 2048 records of the I and Q components last 10.24 ms or about 4-periods of the vibrating tuning fork. This result is demonstrated in Fig. 7 in which a SAW delay line is glued on one prong of a tuning fork³⁸ using Epotek H760E epoxy glue, hence illustrating the full speed measurement capability only limited by the delay line length. Faster refresh rates are accessible with shorter delay lines: since here an 8-bit tag is used the length is defined by the number of echoes, but sensing capability only does not require such long delay lines and shorter designs (with maximum echo delays of 500 ns for example) with only two reflectors as needed for a single physical quantity monitoring will provide higher refresh rates.

[FIG. 6 about here.]

Signal processing for physical quantity extraction is based on the classical phase detection of the echoes: since the two way trip of the acoustic wave along a path of length $2d$ is $2d/c$, the phase shift φ with respect to the local oscillator (as obtained through the I/Q demodulator during the mixing process) is

$$\varphi = 2\pi \cdot 2d \cdot f/c$$

where f is the probed pulse central frequency. Getting rid of any drift of the local oscillator (f) as well as of the distance between the reader and the sensor in case of mobile target

applications requires a differential approach in which the phase *difference* between two echoes is processed. This computation is challenging since the phase extraction from the I and Q components requires obtaining the arctan function, even though a feedback control aimed at keeping a constant strain with a quadrature condition only requires to keep the Q component close to 0 and the I component maximized, with no need to actually compute the associated phase. In the previous example, I and Q data were considered sufficient to provide an estimate of the acoustic velocity c since over the short time scales of a few tens of milliseconds, the local oscillator drift is neglected.

[FIG. 7 about here.]

The need for high bandwidth measurement of vibrating structure mode identification is emphasized in Fig. 8 which exhibits the spectral analysis of the impulse response of the tuning fork fitted with the acoustic delay line strain gauge. In such a case, beyond the periodic analysis of the forced regime response of the tuning fork at its fundamental resonance frequency, high frequency modes are excited as well during the impulse response generation. As seen in the spectral analysis (Fig. 8, main graph) of the time-domain record (Fig. 8, insets), higher order modes³⁸ are well observed up to 40700 kHz, at frequencies consistent with a tuning-fork emitted sound record sampled using a personal computer sound-card acquiring at a rate of 96 kS/s. Identifying the prong motion characteristics of each one of these modes with respect to tabulated overtones is challenging due to the asymmetry between the two prongs introduced by the instrumentation process (adding the delay line transducer on the base of one prong and the target at the end of the other prong) which moves the resonance modes away from the expected frequencies modeled for a symmetric tuning fork³⁸. Nevertheless, a comparison of Figs. 8 and 3 illustrates consistency within the common frequency range (fundamental mode around 440 Hz and overtone around 10 kHz) and an inconsistency for a mode located around 5.5 kHz which is observed by the SAW strain gauge (and the sound card records, not shown) but not by the optical methods. We attribute this mode to a non-flexural mode of the tuning fork, which is detected by acoustic means since the SAW strain gauge is sensitive to multiple components of the strain field, while the image processing method is only sensitive to the displacement component orthogonal to the patterned lines. Hence, any displacement along the lines cannot be detected by the image processing method.

C. Strain measurement resolution assessment

A significant hindrance of pulsed mode RADAR operating at temperature T is the wide integration bandwidth B which brings thermal noise $k_B \cdot T \cdot B$ on the low noise amplifier receiver stage, with $k_B = 1.38 \cdot 10^{-23}$ J.K⁻¹ the Boltzman constant, over a much wider frequency range than FMCW or narrowband resonator based measurement techniques. One might wonder then whether the gain in refresh rate was achieved at the loss of resolution. In both FMCW and pulsed mode RADAR, the phase of the signal within the returned echoes is extracted by mixing with the local oscillator which has drifted over the propagation delay by a quantity σ_φ defined as the phase noise of the local oscillator whose spectral density over a bandwidth BW is defined as $S_\varphi = \sigma_\varphi^2/BW$ rad²/Hz.

Two conditions are considered: since the phase noise of a broadband amplifier (in our case the low noise receiver amplifier – LNA) rises with the received power, at close range the mixer output signal phase noise is defined by the local oscillator phase noise, while at longer range the received power is low enough that the LNA output phase noise drives the phase detection capability. In the latter case in which the receiver LNA overwhelms the local oscillator phase noise, a power P received by an antenna connected to an amplifier at temperature T and characterized by a noise factor F induces a white phase noise floor of $S_\varphi = k_B \cdot T \cdot F/P$. Since no signal extraction is performed (e.g. using a cross correlation technique) to extract the returned signal from the noise, the received power is necessarily above the thermal noise power integrated over the analyzed bandwidth: in the case of a 500 MS/s A/D converter recording the 80 MHz-wide signal, $BW = 40$ MHz and $k_B \times T \times BW = -98$ dBm. In such a condition, the phase noise floor is $S_\varphi = -76$ dB.rad²/Hz or a phase standard deviation $\sigma_\varphi = \sqrt{S_\varphi \cdot BW}$, of 1.0 rad. Since the relative stress sensitivity along the propagation axis of lithium niobate YXl/128° cut we considered is³⁹ $df/f = dc/c = -4.26$ ppm/MPa, operating at 2450 MHz yields a phase to stress sensitivity $d\varphi = 2\pi \cdot 2d \cdot f \cdot dc/c^2 = 2\pi \cdot \tau \cdot dc/c$ where $\tau = 2d/c$ is the echo delay, so by normalizing with τ expressed in μ s, $d\varphi/\tau = 0.0656$ rad.MPa⁻¹. μ s⁻¹ and the measurement resolution at the detection limit of the returned signal is $1/0.0656=15$ MPa for an echo delayed by 1 μ s. In the case of our experiment in which +18.5 dBm were emitted and 43 dB loss were exhibited by the acoustic delay line, the returned power to the first low noise amplifier is -24.5 dBm, hence a phase resolution of 0.21 mrad and the stress resolution is improved to $0.21 \cdot 10^{-3}/0.0656 = 3.10^{-3}$ MPa=3 kPa

or much less than the expected hundreds of kPa induced by the tuning fork vibration³⁵.

In the case of a significant power returned by the sensor, the LNA phase noise is not the driving factor on the phase detection uncertainty which is then driven by the local oscillator. Assuming a flat, white, phase noise in the 100 kHz (refresh rate and hence lower boundary of the integral limits) to above 10 MHz (upper limit of the phase noise characterization) decade equal to a value b_0 , then the phase fluctuation due to the local oscillator is $\sigma_\varphi = \sqrt{b_0 \times BW}$. This analysis is numerically applied to the case of an Agilent E5071B network analyzer whose phase noise is measured (using an Agilent E5052B signal source analyzer) to be around -100 dBc/Hz in the 100 kHz-1 MHz region when providing a continuous output signal at 2.475 GHz (0 Hz span). Under such considerations (signal to noise ratio being unity), the phase measurement resolution is $\sigma_\varphi = 0.06$ rad and the stress resolution is 1 MPa, the poor performance being associated with a rather low quality source provided by the network analyzer. A better source exhibiting a noise floor of $b_0 = -140$ dBc/Hz would provide a resolution of 10 kPa.

It might be worth considering at which distance the transition from one regime to another occurs. The RADAR equation applied to a wireless acoustic sensor exhibiting acoustic loss IL – acting as an equivalent RADAR cross section when combined with the signal wavelength to account for the antenna aperture – states that the receiver power P_R is related to the emitted power P_E when a target is located at distance d from the reader by

$$P_R = P_E \frac{\lambda^4}{IL \cdot (4\pi)^4} \cdot \frac{1}{d^4}$$

with λ the incident pulse wavelength, so the transition distance occurs when the LNA phase noise $k_B T / P_E$ reaches the local oscillator noise b_0 : $b_0 = \frac{k_B \cdot T \cdot d^4 \cdot IL \cdot (4\pi)^4}{P_E \lambda^4}$ which is solved for a distance $d^4 = \frac{P_E \cdot \lambda^4 b_0}{k_B \cdot T \cdot IL \cdot (4\pi)^4}$. A numerical application for $b_0 = -100$ dBc/Hz and $\lambda = 12.2$ cm shows that the critical distance is $d = 12$ cm. A high quality oscillator exhibiting $b_0 = -150$ dBc/Hz reduces this range further to 7 cm, so the LNA will be the dominant issue in most practical applications. The maximum interrogation range is reached when the returned signal reaches the thermal noise, hence a distance d so that $P_R = k_B \cdot T \cdot BW$ with again $BW = 40$ MHz the transducer bandwidth, or 50 cm when emitting $P_E = 10$ mW (Fig. 9). This range extends to 88 cm when emitting 100 mW and 1.2 m if the insertion loss is reduced from 40 dB to 35 dB. All these considerations assuming isotropic antennae fitting the interrogation unit and the sensor: practical applications must account for antenna gain

which, at such a frequency, typically reaches 8.5 dB for antennae with λ^2 area (e.g Huber-Suhner model SPA-2400, Switzerland) and additionally extends the interrogation range for dedicated setups (with the last numerical application, to 3.1 m). Considering the 1% duty cycle, compliance with outdoor emission regulations in the 2.45 GHz band (mean power of 10 dBm) allows for a peak power of $P_E = 1$ W, yielding a 5.5 m expected measurement range, consistent with experimental demonstrations.

[FIG. 8 about here.]

[FIG. 9 about here.]

IV. CONCLUSION

In the context of contactless vibrating structure characterization, we demonstrate real time image or radiofrequency signal processing for measuring displacements up to several kHz sampling rates with real time processing compatible with feedback control. The implementation of the pulsed mode RADAR with 500 MHz sampling rate (2 ns sample period) allows for the identification of the velocity of a mechanical wave in an acoustic delay line acting as a passive, wireless sensor configuration with a measurement speed only limited by the acoustic wave echo delay. We demonstrate a 250 kHz refresh rate, only limited by the 4 μ s acoustic delay due to the 8 mm long acoustic path of the SAW tag used in this experiment, which could be increased further by using a dedicated SAW sensor design with smaller acoustic path length. Digital image processing for extracting pattern motion analysis using a cross-correlation technique was implemented digitally and achieved a refresh rate of 27 kHz. Both measurements performed on a same vibrating structure, a musical tuning fork, provided consistent vibrational mode analysis results. Real time performance yielding sub-10 μ s latencies when generating the computation results can only be achieved by implementing the processing algorithms in Field Programmable Gate Array (FPGA), with latencies only limited by the physical measurement principle (image acquisition or acoustic delay line probing) and not the computation time.

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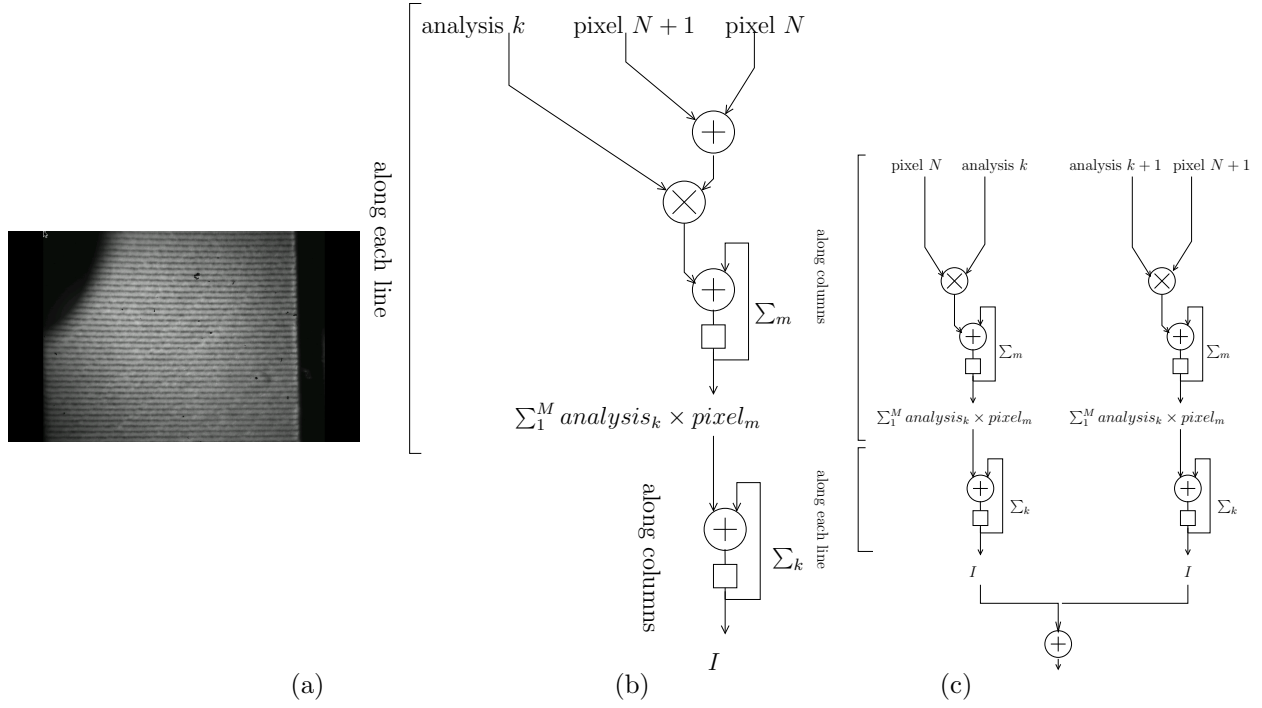


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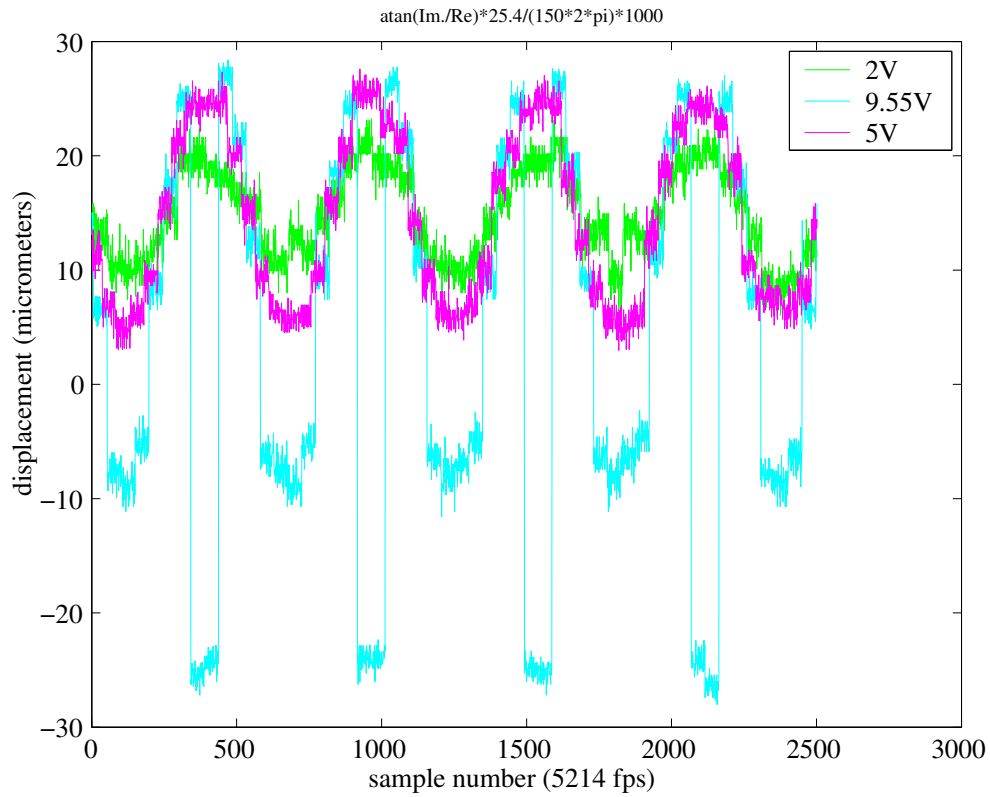


FIG. 2. Phase, computed as $\arctan(Q/I)$, measured for various drive voltages of the voice coil. For the largest driving voltage, the phase rotation of 2π is visible (blue curve). Tuning the phase offset φ_0 maximizes the measurement range by keeping the phase rotation within the $[-\pi/2, \pi/2]$ range.

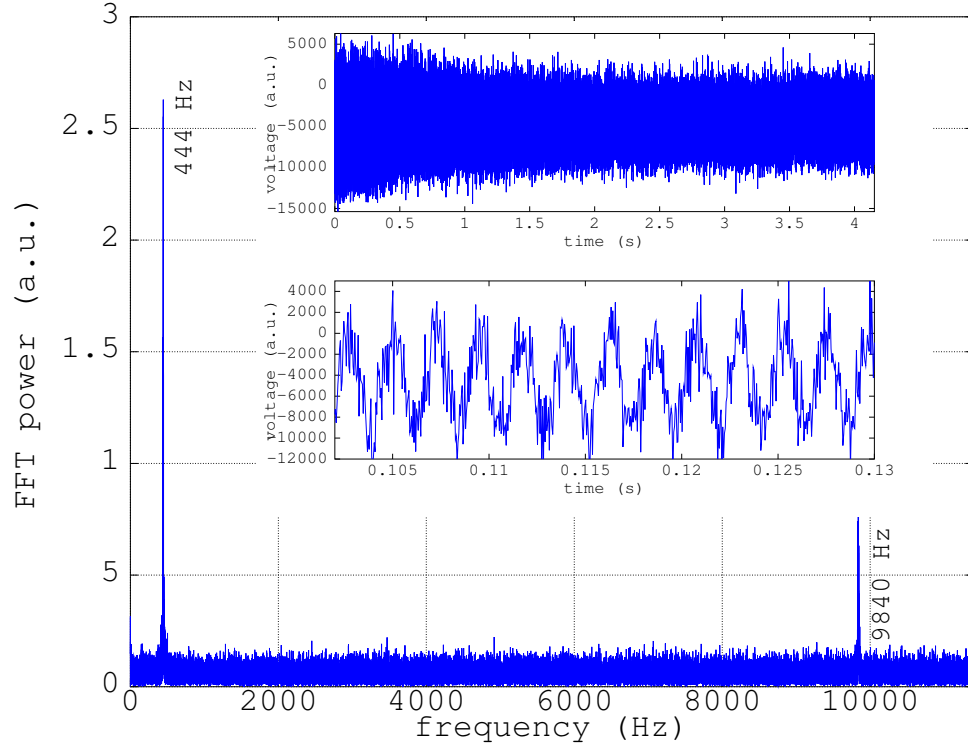


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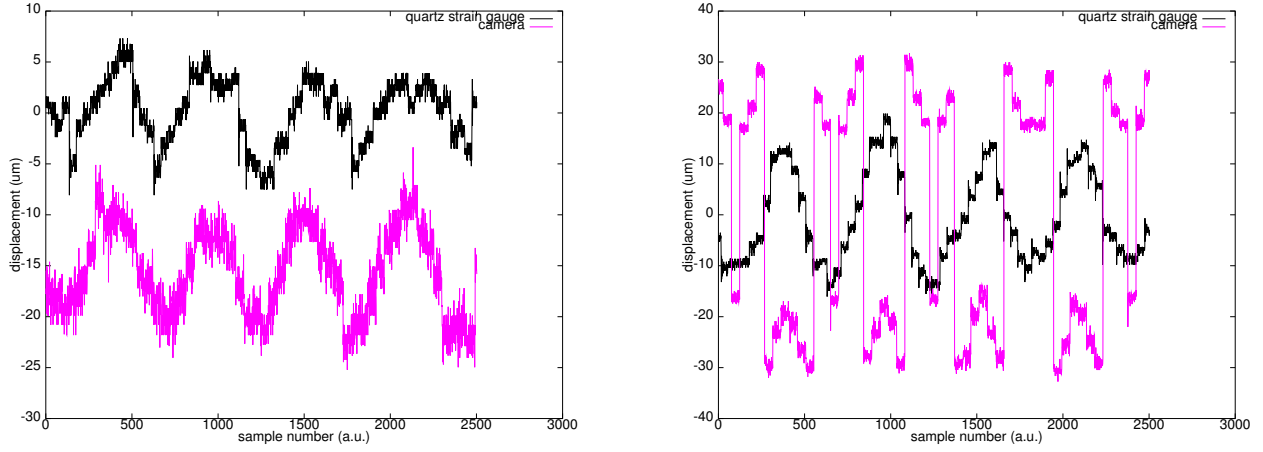


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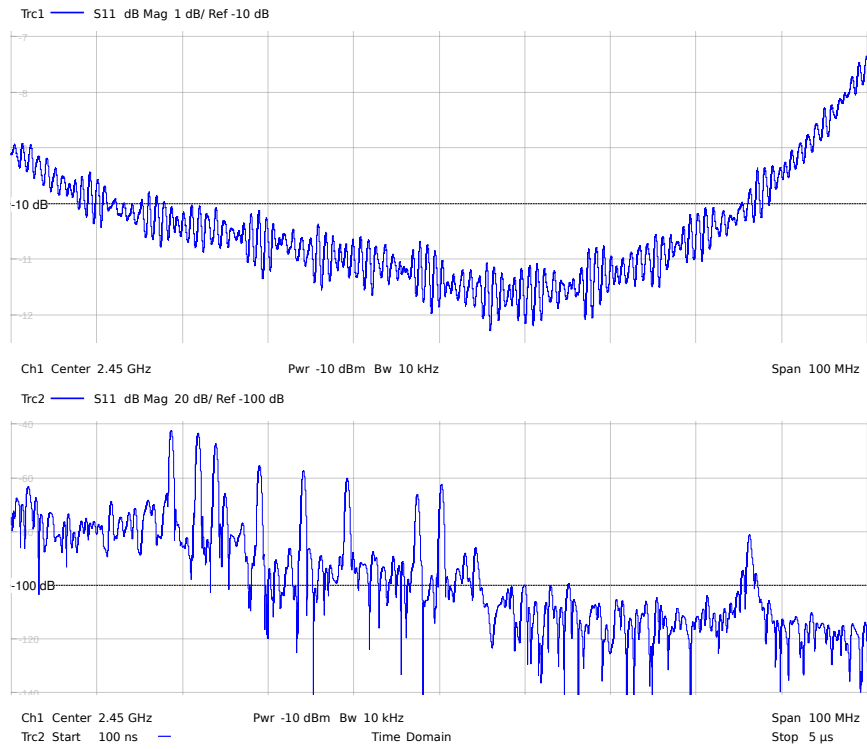


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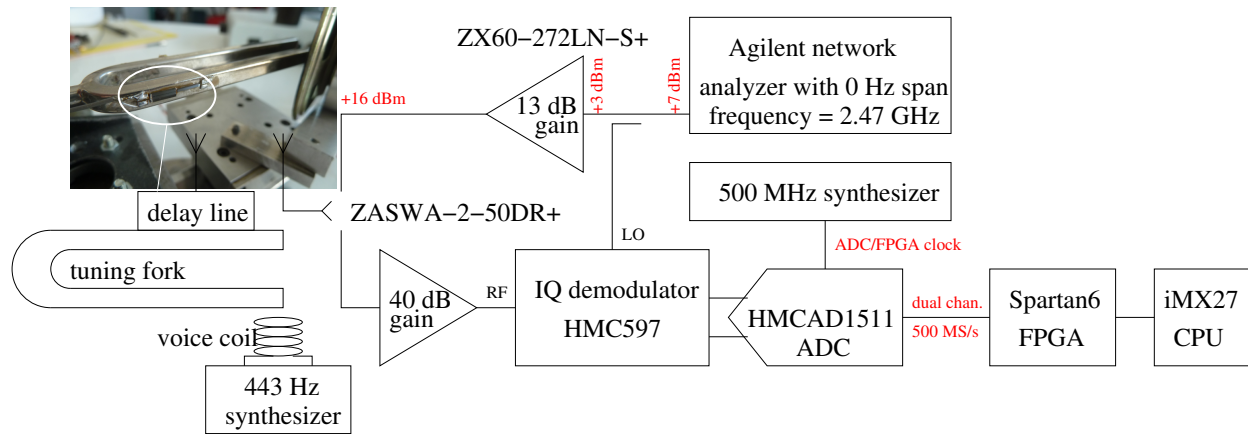


FIG. 6. Schematic of the experimental setup for probing an acoustic delay line response at 250 ksamples/s.

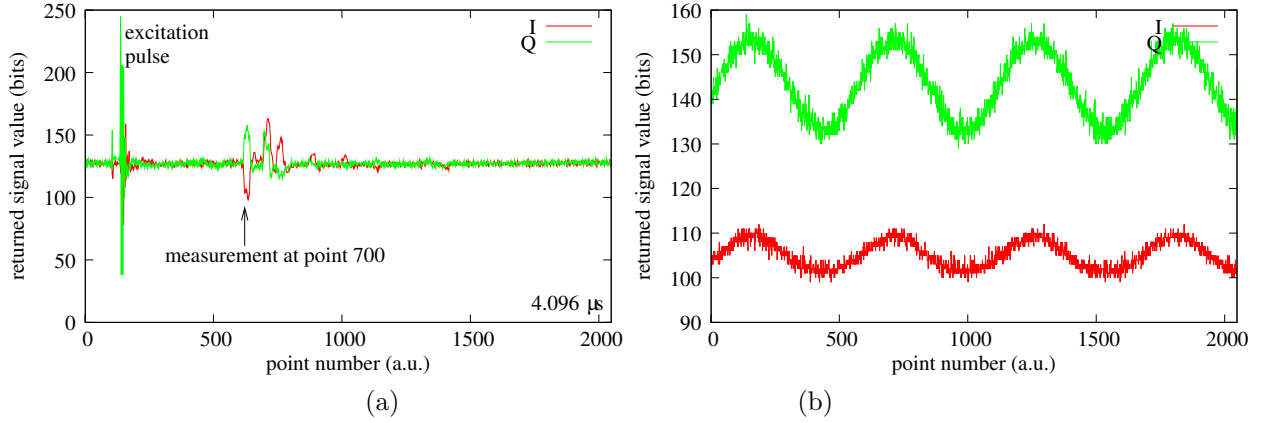


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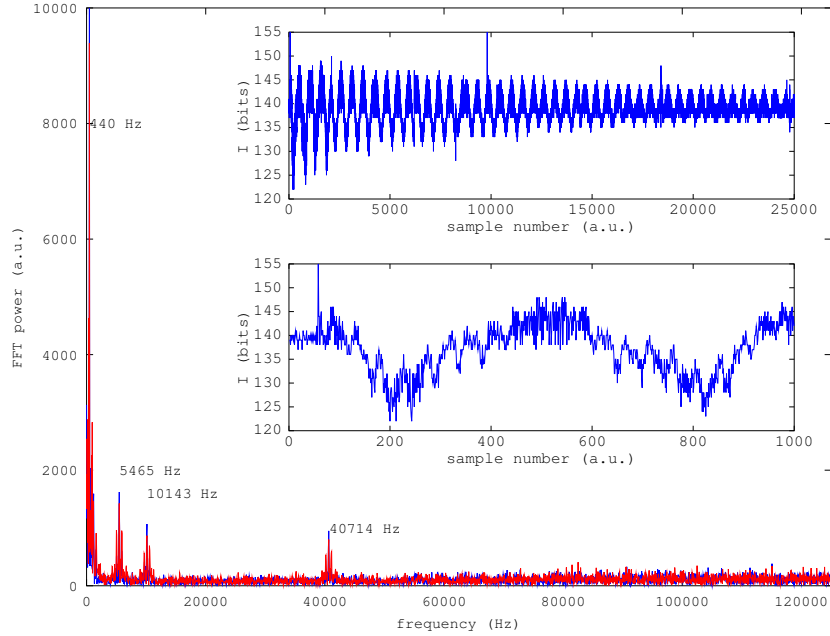


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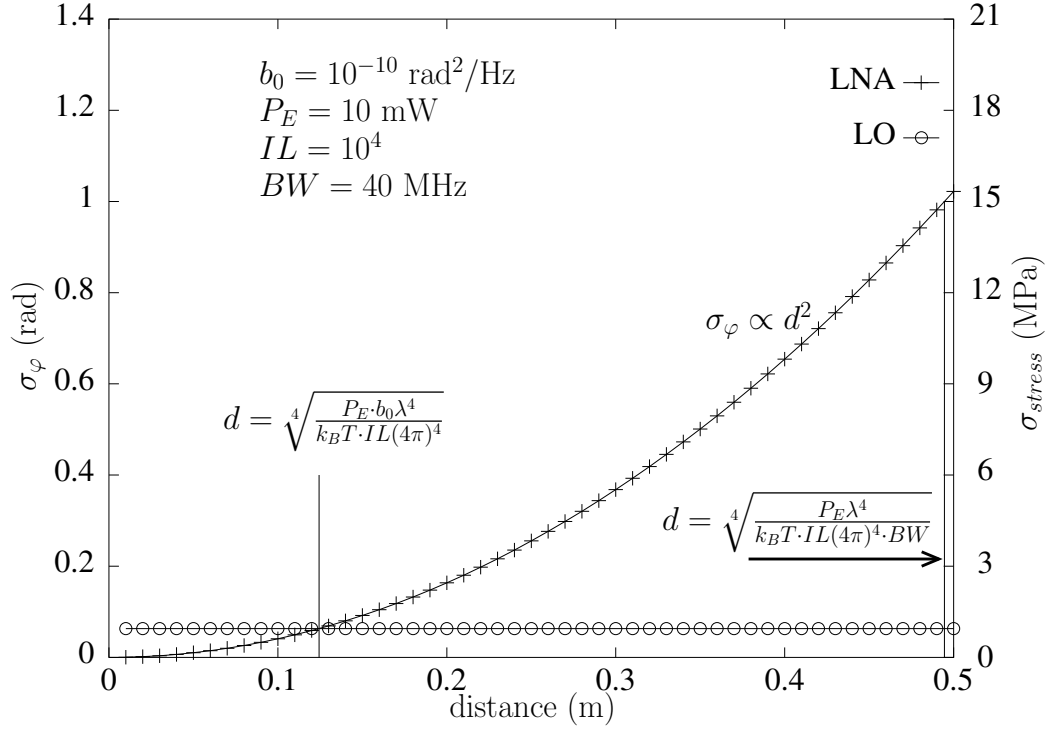


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